

Article-No:

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# CodeMeter ASIC



## Description

The CodeMeter ASIC represents the heart of the CodeMeter Digital Rights Management system. It contains a smart card chip with 1 MByte secure memory, including a minimum of 328 kByte available for storing CodeMeter licenses.

Encryption algorithms used in firmware 4.10 or higher: 128 and 256-bit AES, SHA-256, 1024, 2048 and 4096-bit RSA, 224-bit ECC.

The integrated smart card chip contains a FIPS 140-1 compliant random number generator and also complies with the evaluation level EAL 5+, including the crypto library.

## CodeMeter ASIC 1504-03

CodeMeter ASIC 1504-03 with full CodeMeter functionality, USB and SPI interface, and an integrated Infineon SLM-97CUSIFX1M00 smart card chip, 3.3V VCC. Rated to operate from -40 °C to +105 °C.

The chip uses a 5mm x 5mm chip size VQFN-32 package.

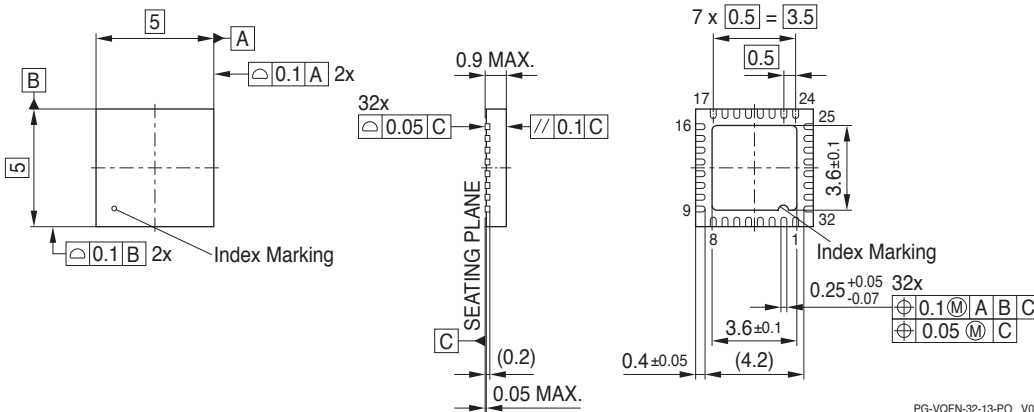
RoHS compliant  
WEEE-Reg-No:  
DE 90465365



## PIN Definition and Function

PIN Definition and Function	Package VQFN-32
VSS (GND)	17
MS Mode Select (GND = SPI, VCC = USB)	19
DM (USB-, leave open if not used)	20
DP (USB+, leave open if not used)	5
Reserved (PIN must be left open)	6
RST (control input, reset signal, active low)	7
VCC (supply voltage)	8
SPI_MOSI (input, slave receive)	10
SPI_MISO (output, slave transmit)	11
SPI_SCK (serial clock, idle high)	12
SPI_CS (chip select, active low)	13

## Technical Drawing



All dimensions in millimeter. Subject to change without notice.

WIBU-SYSTEMS AG  
Rueppurrer Strasse 52-54  
D-76137 Karlsruhe  
Tel: +49-721-93172-0  
Fax: +49-721-93172-22  
info@wibu.com  
www.wibu.com